

FIG 2

(a)

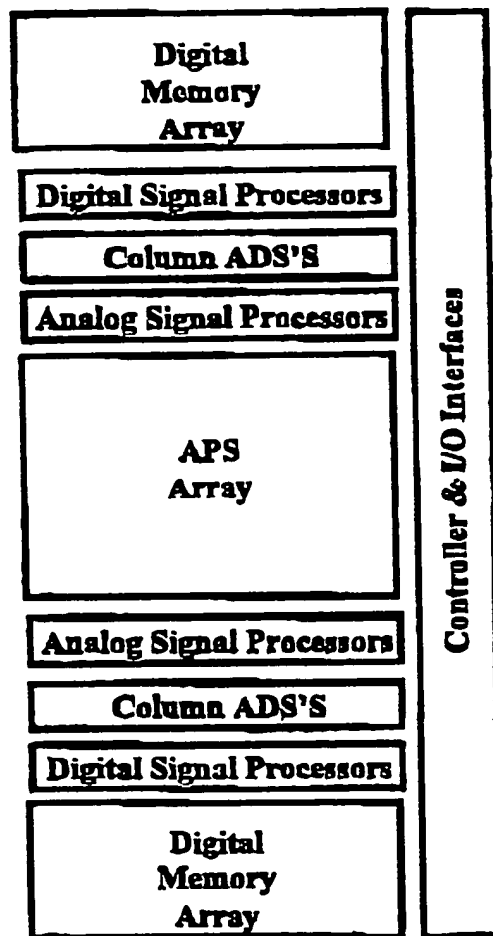


FIG 3

(b)

Figure 1 Block diagram of APS with Mixed Analog and Digital Signal Integration.